

APEC 2011

Advanced Control Architectures for Intelligent Microgrids

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Outline

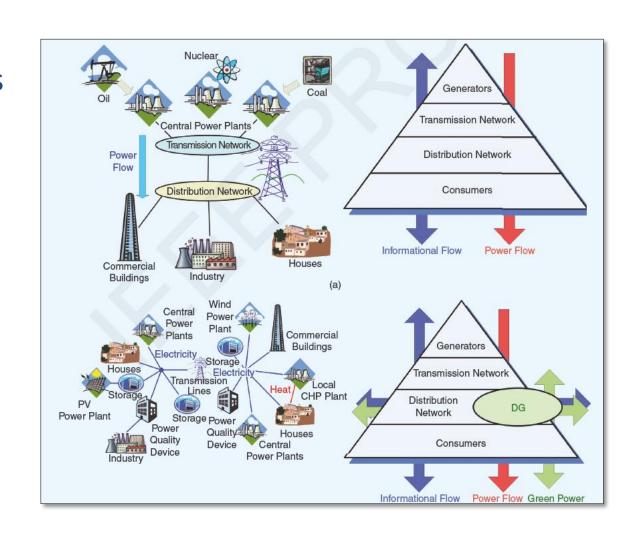
- 1 Microgrids systems
- 2 Control of VSIs for microgrids
- 3 Droop control and virtual impedance concept
- 4 Hierarchical control of microgrids
- 5 Power quality in microgrids
- 6 DC microgrids



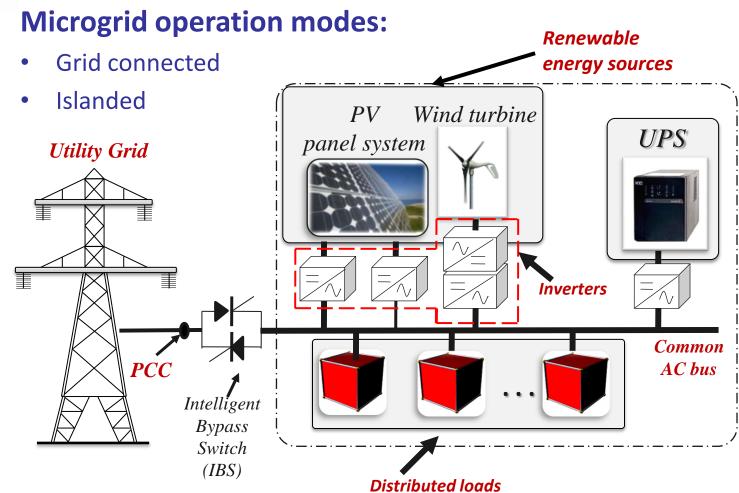
Centralized vs Distribuited Power Systems

General advantages of the DPS:

- Redundancy
- Modularity
- Fault tolerance
- Efficiency
- Reliability
- Easy maintenance
- Smaller size
- Lower design cost







Typical structure of a flexible microgrid



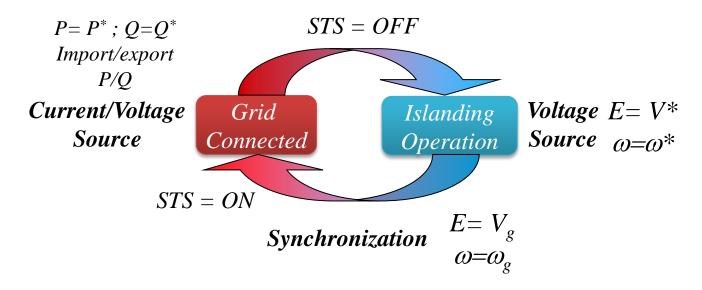
Microgrid operation modes

- Operation modes and transfers of the flexible microgrid and STS grid status supervisory
- Virtual inertias are often implemented through control loops known as droop method.
- Intelligent microgrids are required to integrate DG, DS, and dispersed loads into the future smart grid.
- Microgrids should be able to operate autonomously but also interact with the main grid.
- CSI units are normally used for PV or WT systems that require maximum power point tracker algorithms.
- VSI units are used for storage energy systems to support the voltage and frequency of the microgrid in island mode.



Islanded / Grid-connected operation

 Operation modes and transfers of the flexible microgrid and Static Transfer Switch (STS)

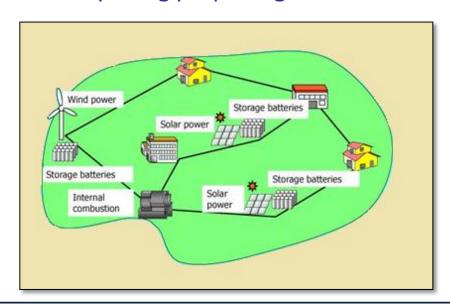


From grid-connected an islanded modes, it is necessary a smooth transition. For both modes, the converters could work as voltage sources!



Islanded operation

- Preplanned islanded operation: If any events in the main grid are presented, such as long-time voltage dips or general faults, among others, islanded operation must be started.
- Nonplanned islanded operation: If there is a blackout due to a disconnection of the main grid, the microgrid should be able to detect this fact by using proper algorithms.





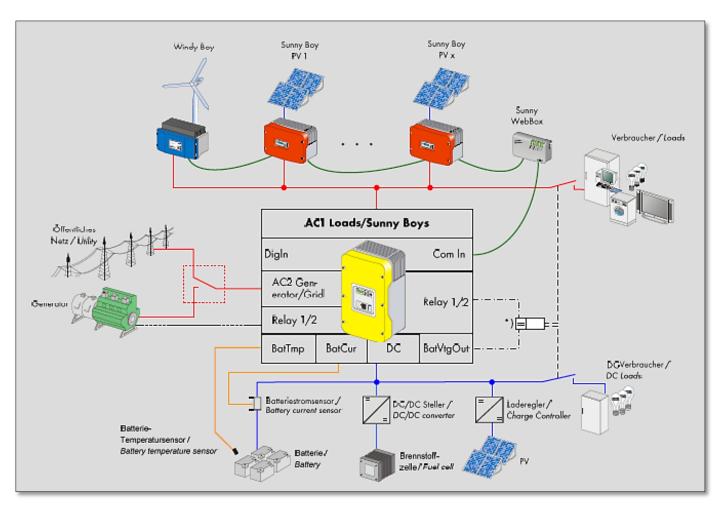
Islanded operation

- Voltage and frequency management: The system acts like a voltage source, controlling power flow through voltage and frequency control loops adjusted and regulated as reference within acceptable limits.
- **Supply and demand balancing**: In grid-connected mode, the frequency of the DG units is fixed by the grid. Changing the setting frequency, new active power set points that will change the power angle between the main grid and the microgrid can be obtained.
- **Power quality**: The power quality can be established in two levels. The first is reactive power compensation and harmonic current sharing inside the microgrid, and the second level is the reactive power and harmonic compensation at the PCC; thus, the microgrid can support the power quality of the main grid.



Microgrid Configurations

• AC-DC Hybrid Microgrid Hierarchy of loads

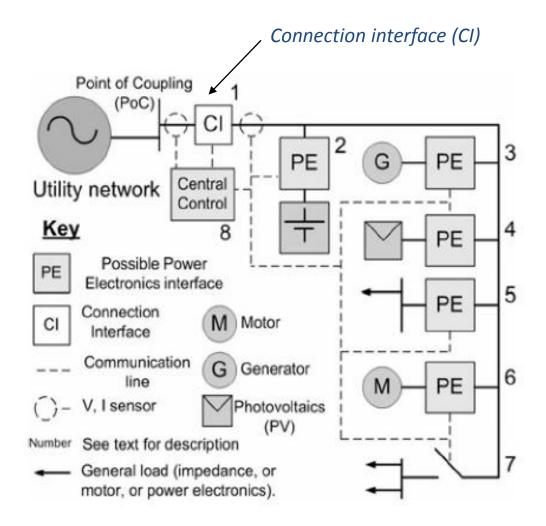


Source: SMA

18-Aug-11



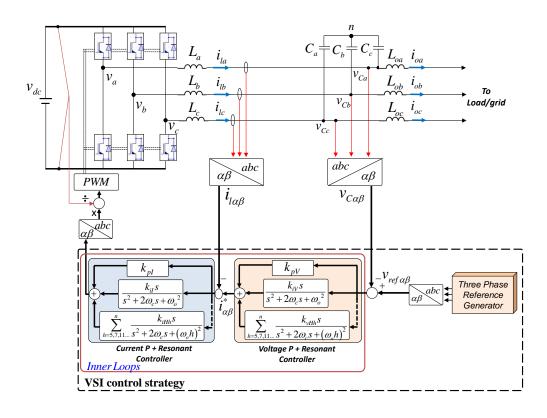
Microgrid Configurations

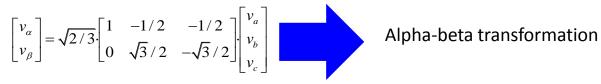




Inner control loops

Power stage and control of a 3 phase VSI with LCL filter

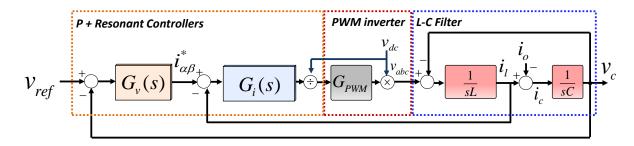






Inner control loops

Block diagram of the closed-loop VSI.



$$V_{c} = \frac{G_{v}(s)G_{i}(s)G_{PWM}}{LCs^{2} + (Cs + G_{v}(s))G_{i}(s)G_{PWM} + 1}V_{ref} - \frac{\frac{1}{Cs}}{LCs^{2} + (Cs + G_{v}(s))G_{i}(s)G_{PWM} + 1}i_{o}$$

Voltage tracking

Output impedance

Voltage control loop

$$G_{v}(s) = k_{pV} + \frac{k_{rV}s}{s^{2} + \omega_{o}^{2}} + \sum_{h=5,7,11} \frac{k_{hV}s}{s^{2} + (\omega_{o}h)^{2}}$$

$$G_{i}(s) = k_{pI} + \frac{k_{rI}s}{s^{2} + \omega_{o}^{2}} + \sum_{h=5,7,11} \frac{k_{hI}s}{s^{2} + (\omega_{o}h)^{2}}$$

Current control loop

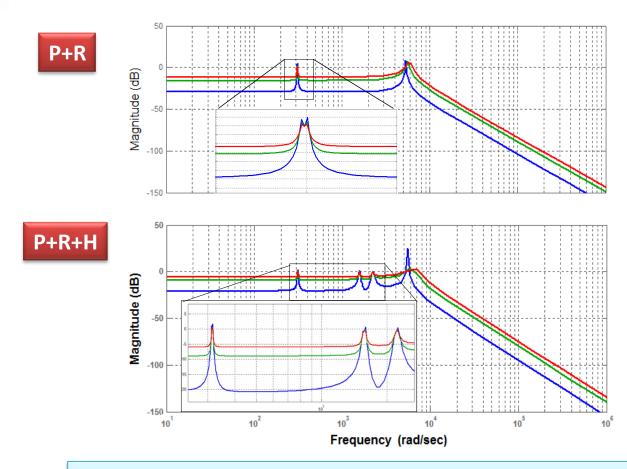
$$G_i(s) = k_{pI} + \frac{k_{rI}s}{s^2 + \omega_o^2} + \sum_{h=5,7,11} \frac{k_{hI}s}{s^2 + (\omega_o h)^2}$$

Computation delay
$$G_{PWM} = \frac{1}{1+1.5T_s}$$



Inner control loops

Bode diagram of the tracking voltage transfer function Gv(s)

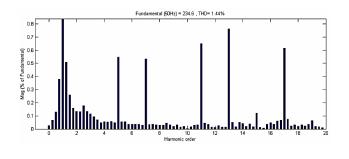


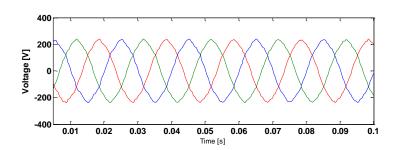
Objective: closed-loop band pass filter characteristics with 0dB, 0º

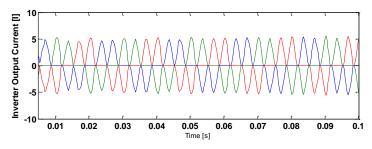


Inner contro loops

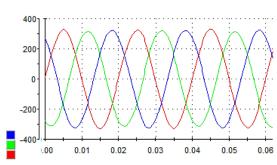
Results

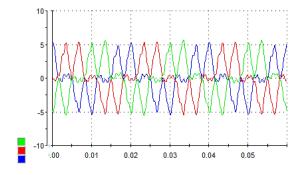






abc-Voltages



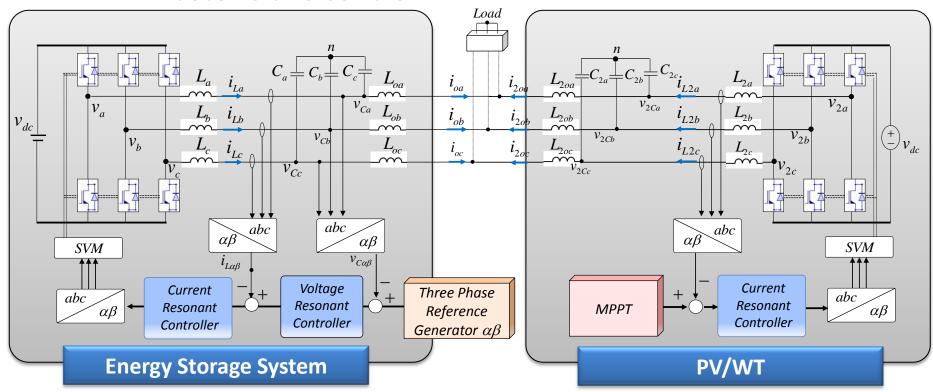


abc-Currents



Control of parallel converters

Master-slave control



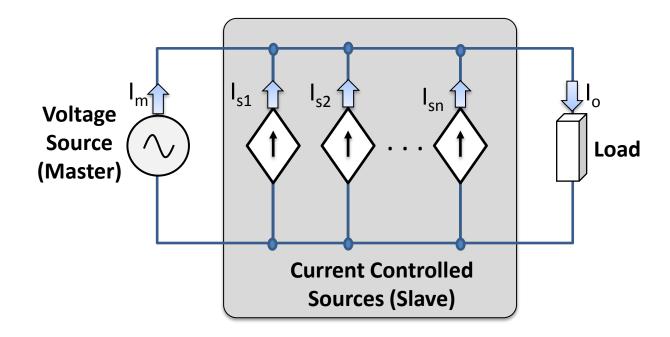
- Voltage source: grid forming units
- Current source: MPPT units. WT and PV

In this system is not necessary current sharing!



Control of parallel converters

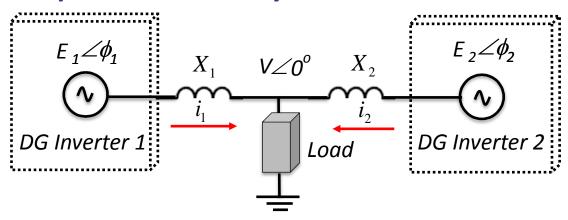
Master-slave control



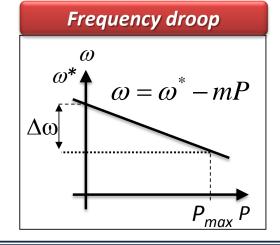
Woo-Cheol Lee "A Master and Slave Control Strategy for Parallel Operation of Three-Phase UPS Systems with Different Ratings"

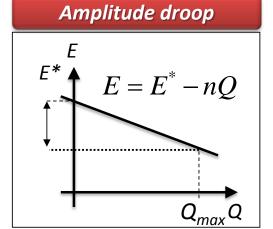


Droop control of AC systems



Active power
$$P = \frac{VE}{X} \sin \phi$$
 Reactive power $Q = \frac{EV \cos \varphi - V^2}{X}$





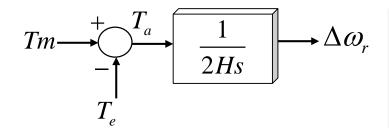


Inertias in power systems

Synchronous generator

Equation of motion:
$$J \frac{d\omega}{dt} = T_m - T_e$$

Inertia constant:
$$H = \frac{stored\ energy}{rating\ power} = \frac{E}{P}[s] \text{ with } E = \sum \frac{1}{2}J\omega^2$$



S Laplace Operator

Tm Mechanical torque (pu)

 T_e Electrical torque(pu)

 $T_{\scriptscriptstyle a}$ Accelerating torque (pu)

H Inertia constant (MW-Sec/MVA)

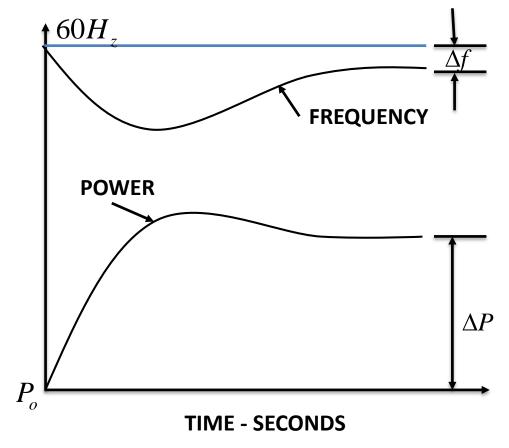
 $\Delta\omega_r$ Rotor speed deviation (pu)

P. Kundur



Inertias in power system

Synchronous generator transient response



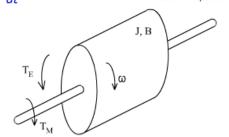
There is a dynamic and a static droop. The static droop coeficient is $\Delta P/\Delta f$.

OF NMART

Virtual synchronous generators

Synchronous Machine

$$J\frac{d}{dt}\omega = T_m - (T_e + B\omega)$$
 Synchronous Machine Motion Equation

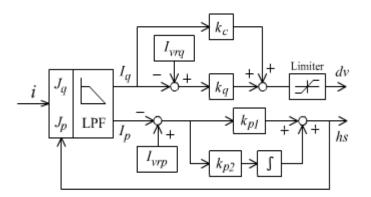


• Inverters that mimic synchronous converters

Kawamura's approach (2005)

Representation of a synchronous machine

Independent Control

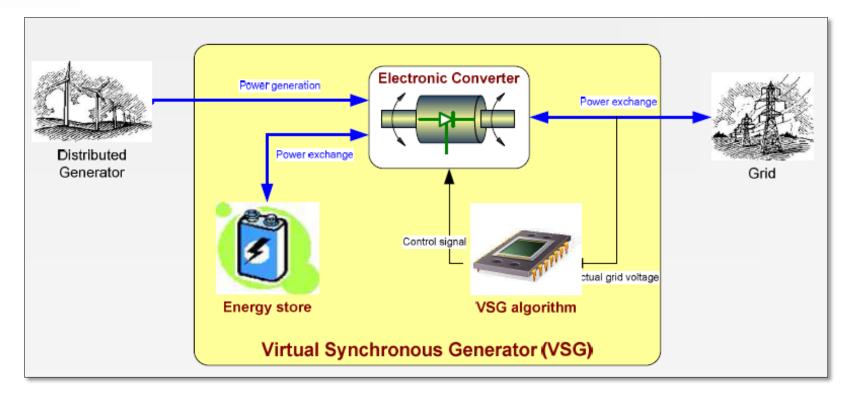


To determine dv and hs (amplitude variation and phase angle, respectively), the controller computes the active and reactive components (I_p and I_q , respectively) of the output current i.

High Reliability and High Performance Parallel-Connected UPS System with Independent Control Eduardo Kazuhide Sato



Virtual synchronous generators



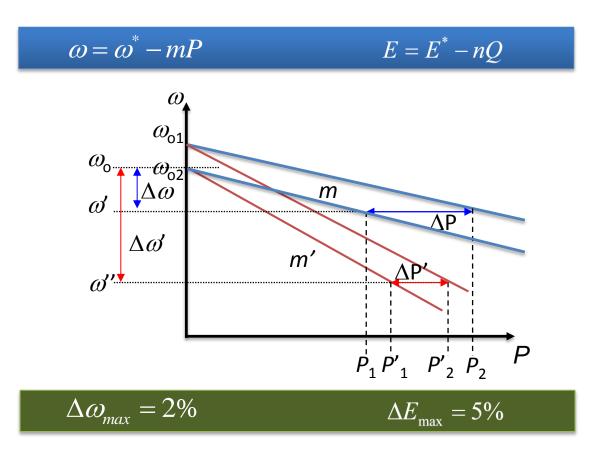
Inertias means not only load-dependent frequency (droops), but also local storage energy system.

European Project VSYNC: http://www.vsync.eu



Droop control of AC systems

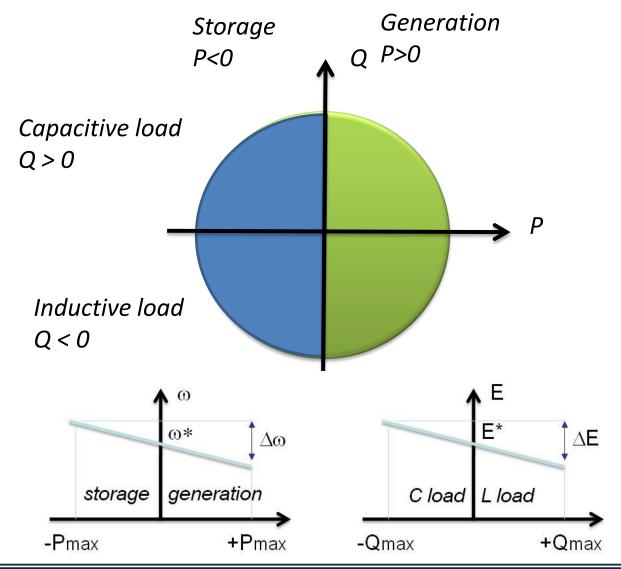
• Trade-off power sharing / amplitude - frequency regulation



Phase droops are not feasible since the initial phase of each inverter is different!



Droop control of AC systems

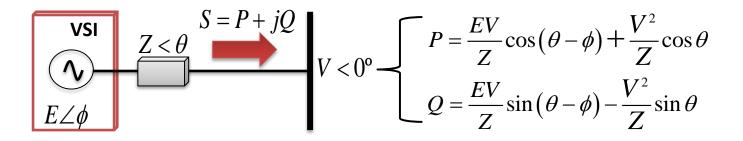


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Generalized droop control

Study of P/Q flow in function of the output impedance



By using the Park transformation, the droop method functions become

$$\omega = \omega^* - m(P\sin\theta - Q\cos\theta)$$

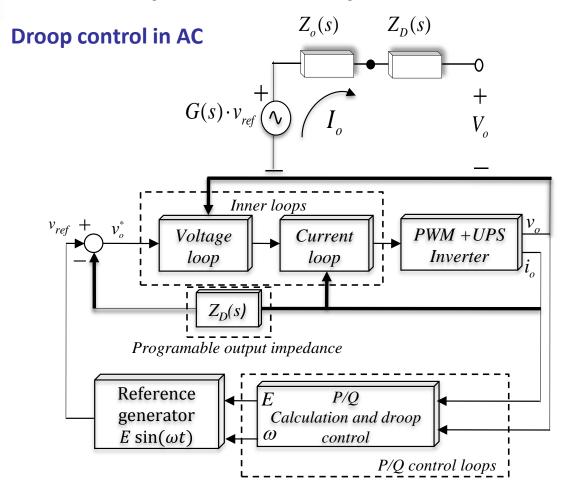
$$E = E^* - n(P\cos\theta + Q\sin\theta)$$

The R – V virtual resistance in a DC microgrid can be see as Q – V droop in an inductive AC microgrid. The ω – P droop is added to synchronize the system.



Virtual Impedance

Virtual Impedance concept

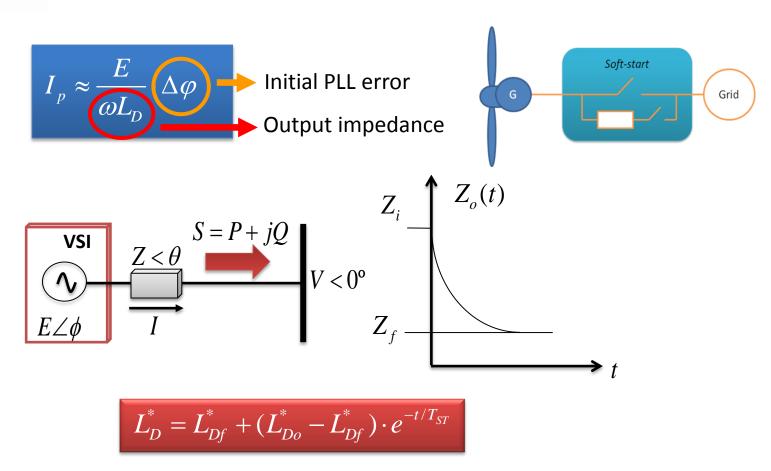


Objective: fix the output impedance



Virtual Impedance

Soft-start operation



The virtual output impedance is a control variable.

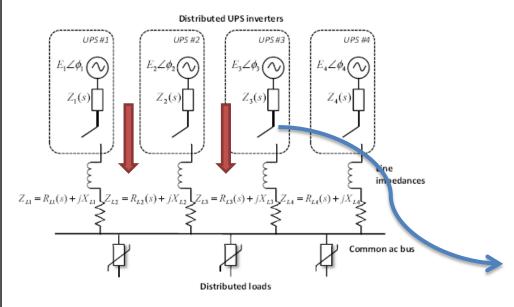
Increasing the output impedance can reduce the initial current peak at the connection

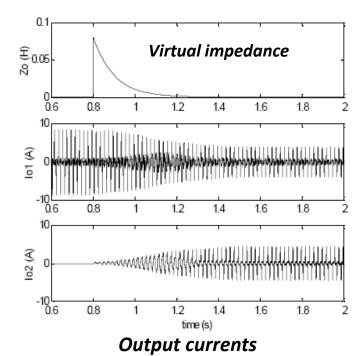


Virtual Impedance

Hot-swap capability

4 DG units microgrid





Before the connection,

a PLL have to synchronize the DG with the MG.

A the connection the virtual impedance is high to reduce the initial current peak.

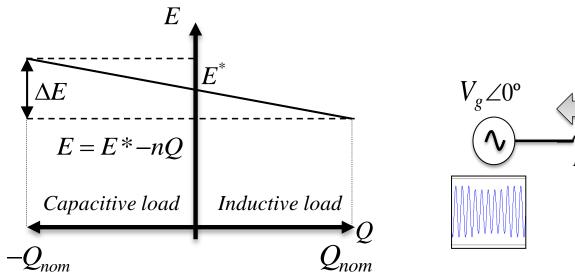
This capability allow us to connect DGs without stop the microgrid, for mantainance reasons.

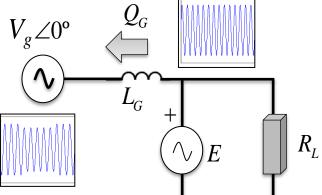


Low voltage ride-though

Low voltage ride-through

Reactive power control of a grid-connected DG.





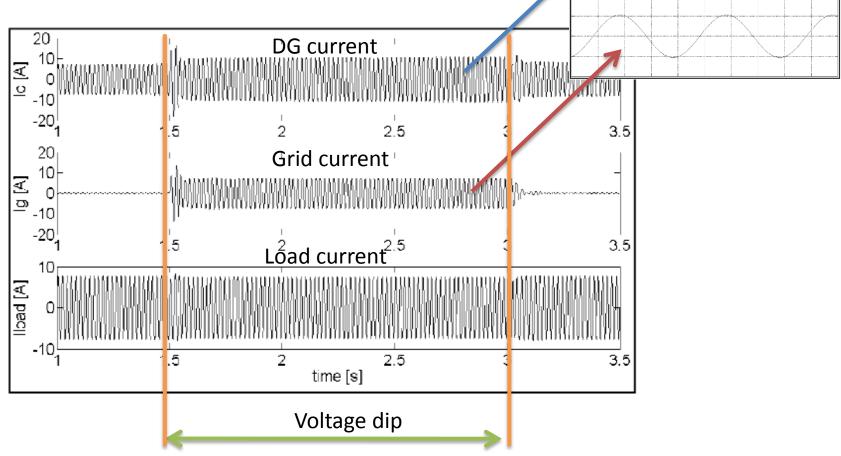
Trade-off during voltage dips: 1) voltage follower (Q=0) 2) stiff voltage source (Q high)



Low voltage ride-though

Low voltage ride-through

Reactive power control of a grid-connected DG.



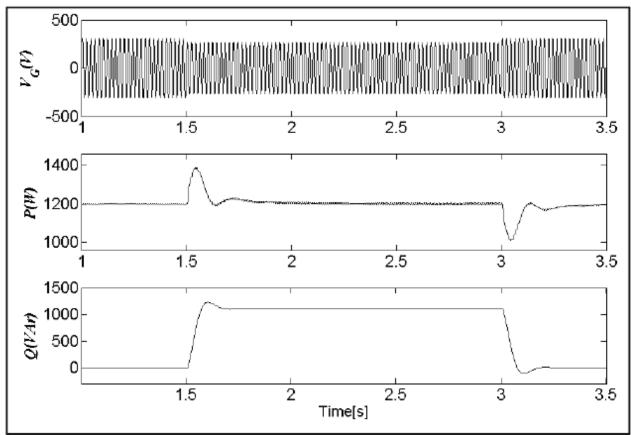
During the voltage grid, the converter injects reactive current (90°)



Low voltage ride-though

Low voltage ride-through

Reactive power control of a grid-connected DG.



Active power remains constant (to the load). Reactive power is injected to mantain the voltage inside the droop characteristic.



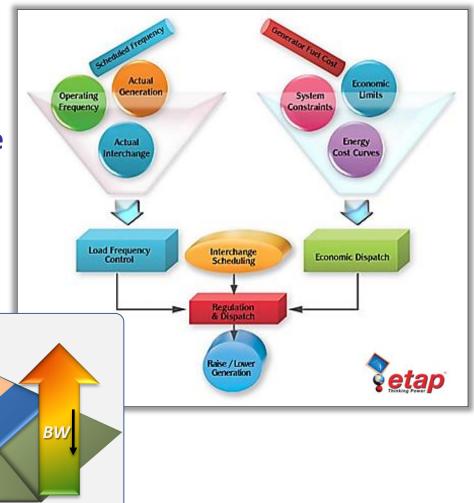
Hierarchical control

Hierarchical
Control
Principle

Tertiary Control

Secondary Control

Primary Control



Enterprise Software Solution for Power Systems

18-Aug-11

Import/export power

Restoration/Syncro.

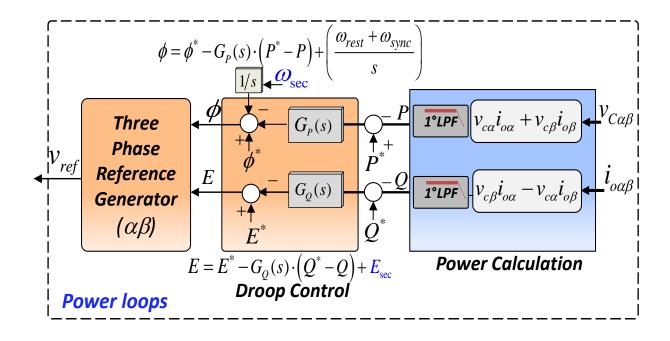
Inner loops

(droop, softstart)



Hierarchical control

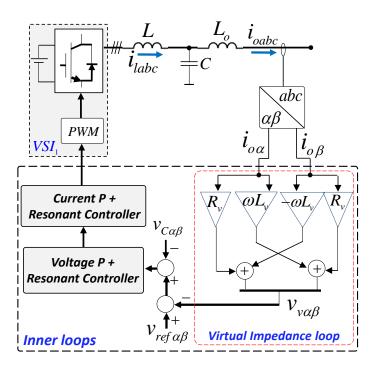
Droop control for three phase VSIs





Hierarchical control

Virtual impedance control for three phase VSIs



$$\begin{cases} V_{V\alpha} = R_{v} \cdot i_{o\alpha} - \omega L_{v} \cdot i_{o\beta} \\ V_{V\beta} = R_{v} \cdot i_{o\beta} + \omega L_{v} \cdot i_{o\alpha} \end{cases}$$

$$G(s)v_{ref} \bigvee_{ref} \begin{matrix} Z_o(s) & Z_D(s) \\ & & + \\ & & V_v \\ & & - \end{matrix}$$

$$v_{out}(s) = G(s)v_{ref} - Z_o(s)i_o$$

$$Z_{o}(s) = \frac{\frac{1}{Cs} + G_{v}(s)G_{i}(s)G_{PWM}(s)Z_{D}(s)}{LCs^{2} + (Cs + G_{v}(s))G_{i}(s)G_{PWM}(s) + 1} + L_{o}s$$



Secondary Control

Islanding microgrids

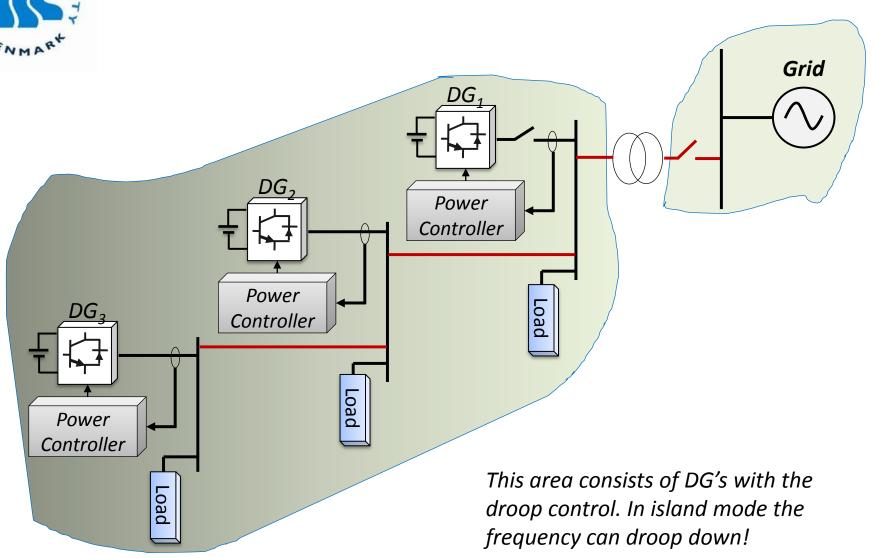
- Grid-connected microgrids operate synchronized with the grid
- Islanded microgrids:
 - Frequency and amplitudes are load-dependent
- Secondary control can contribute to:
 - Frequency restoration
 - Amplitude regulation
 - Power quality (harmonics and unbalance compensation)

Energy management system can be used to:

- Load shedding
- Regulation of the generator's consumption

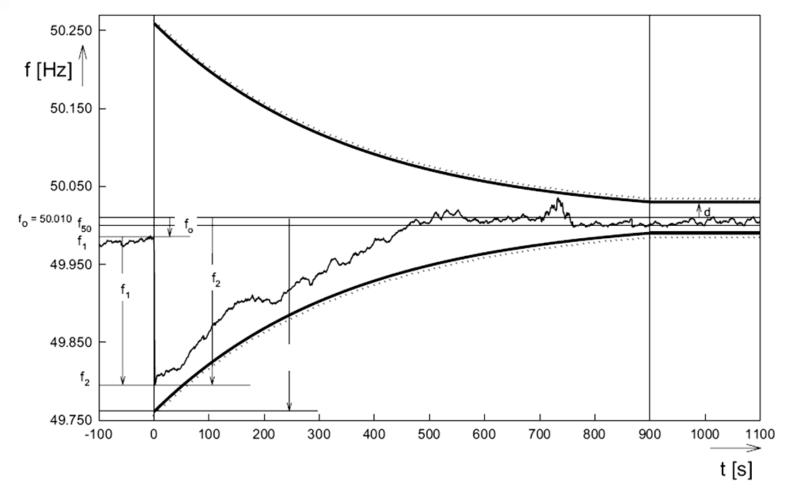


Secondary Control in Electric Power Systems





Secondary Control in Electric Power Systems

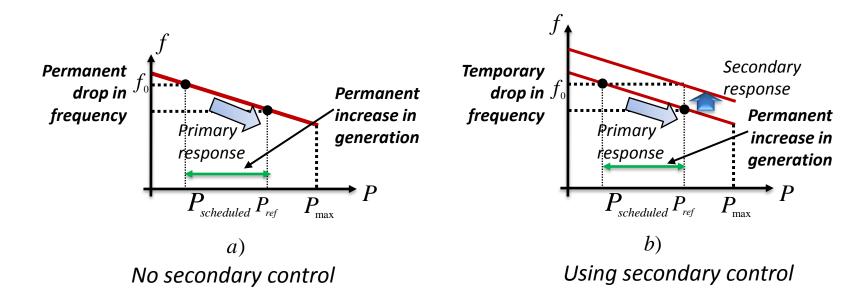


Source: UCTE. A1 – Appendix 1: Load-Frequency Control and Performance



Secondary Control for Microgrids

Secondary control action



Primary control ensures P sharing by drooping the frequency

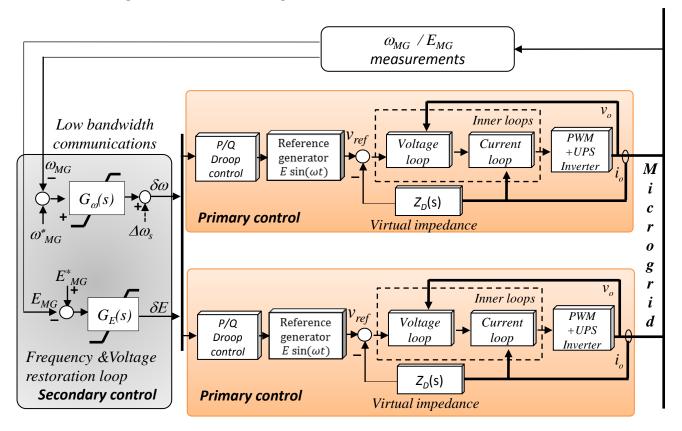
Secondary control:

- Restore the nominal frequency
- Cannot work localy, it needs to be centralized.



Secondary Control for Microgrids

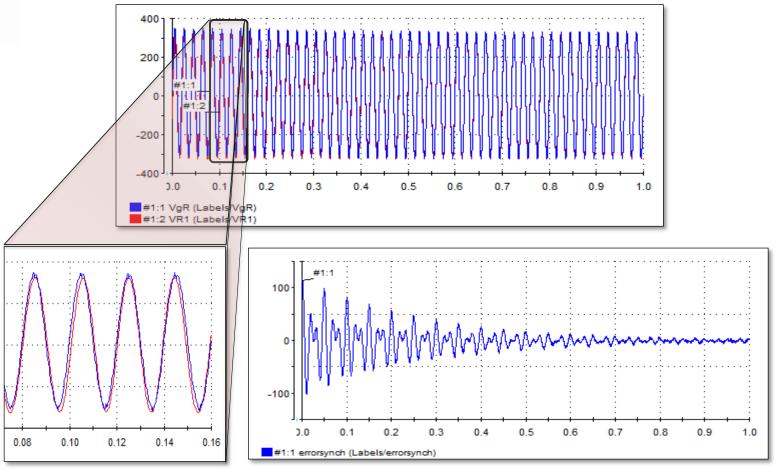
Secondary control Implementation



Secondary control is located in the Microgrid Central Controller measure frequency and voltage. The output of the control is send through communications to adjust the reference of the local primary controllers (*droops*).



Microgrid synchronization with the grid



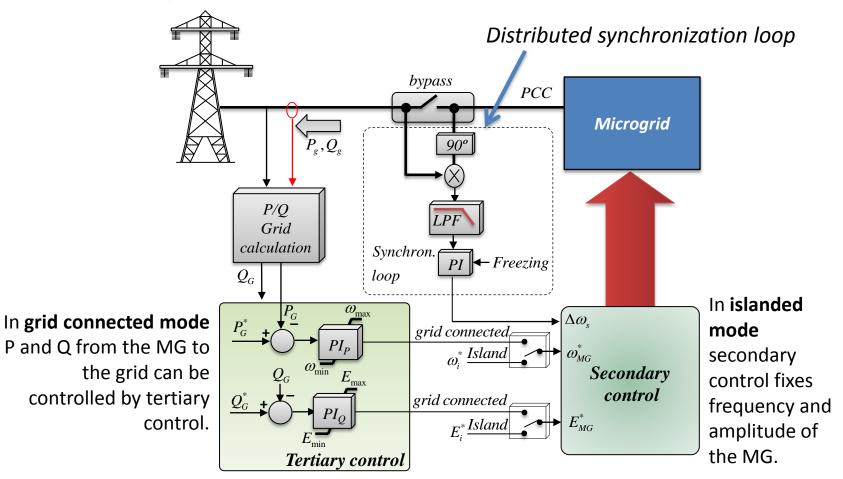
Synchronization is not necessary to be fast. **Slow** (to avoid unstability problems) but well **accurate** (allowing seamless transition to grid-connected mode).



Tertiary Control

Tertiary control for AC microgrids

Terciary control and synchronization control loops implementation

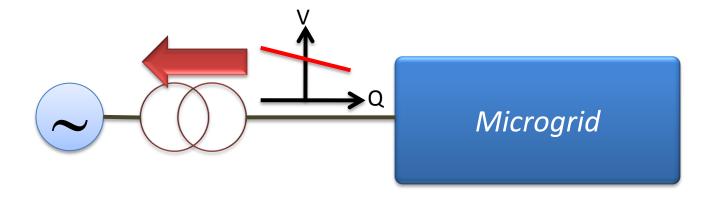




Tertiary Control

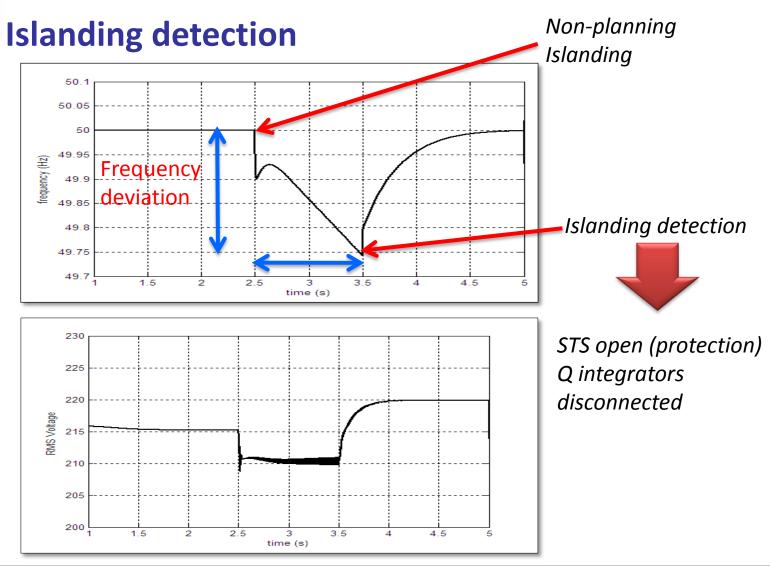
Tertiary control for AC microgrids

- Low voltage ride-trough of the Microgrid
 - Freezing or disconnecting the integral term of the E –
 Q tertiary control.
 - The Microgrid will work like a STATCOM





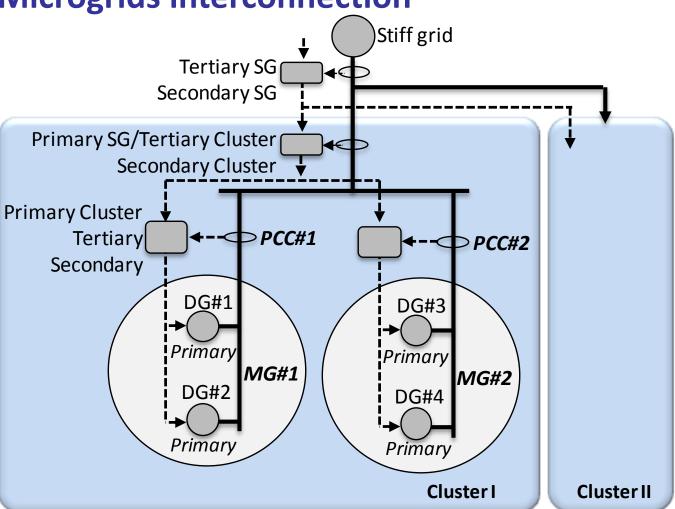
Microgrid example

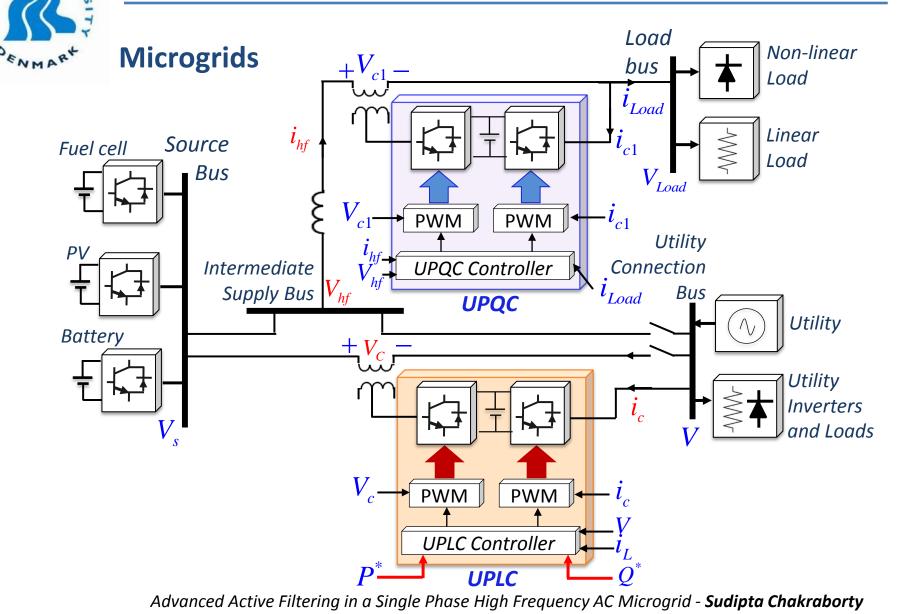




Smart-Grids

Microgrids interconnection

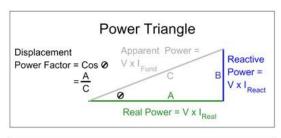


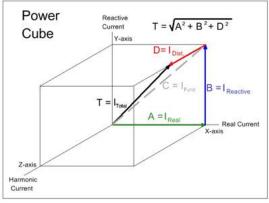




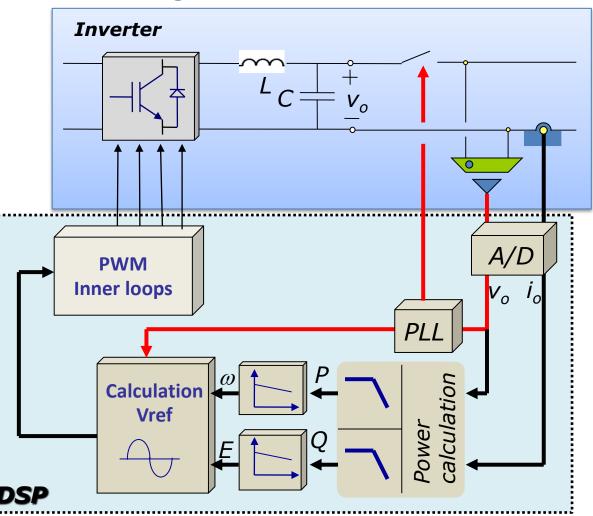
Harmonic current sharing

- Droop control allows P and Q sharing, averaged over the fundamental frequency.
- It is not able to guarantee harmonic current sharing!





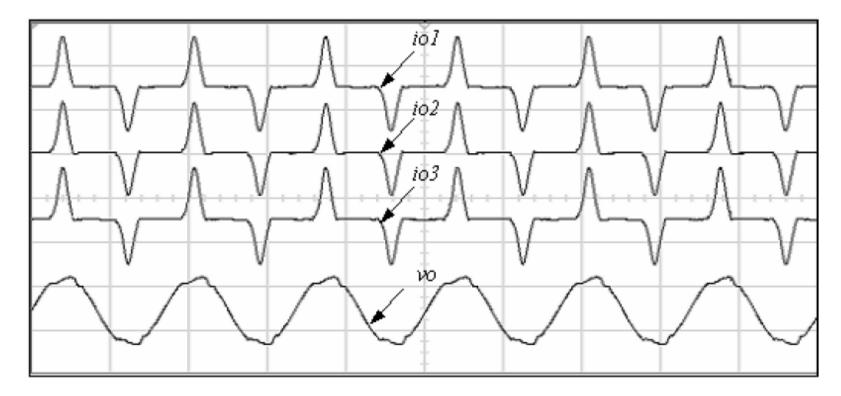
$$D = \sqrt{S^2 - P^2 - Q^2}$$





Harmonics current sharing

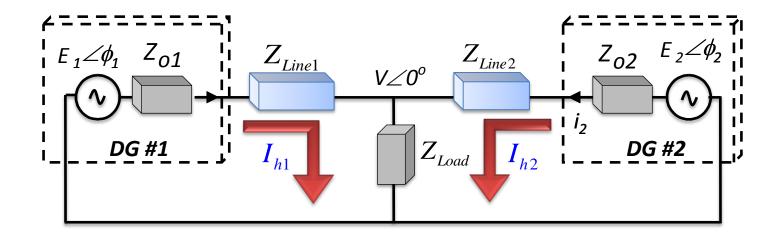
- Control objective: Harmonic current sharing proportional to the nominal DG power.
- Trade off: harmonic current sharing/voltage THD



Source: Y. E. Wu



Harmonics current sharing



- For **Z**_{line1} # **Z**_{line2}, harmonic current sharing is not possible
- Harmonic virtual impedance can enhance sharing

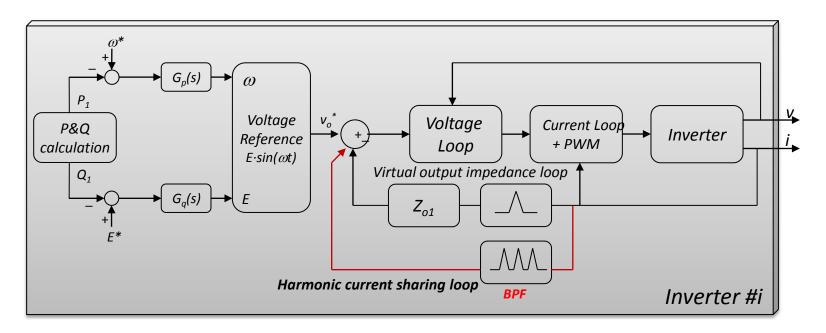
$$Zh = Vh/Ih$$

• Trade off V_{THD} and harmonic current sharing.



Droop control

Virtual Output Impedance with harmonic current sharing loop



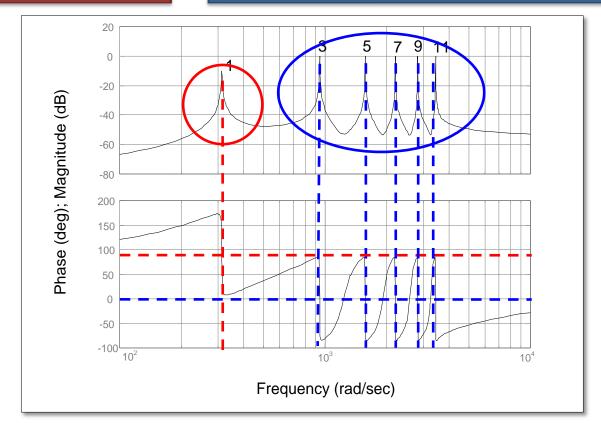
Selective harmonic selection: fundamental and each of the harmonics can have different output impedance.



Harmonic current sharing

$$H_i(s) = \frac{2k_i s}{s^2 + 2k_i s + \omega_i^2}$$

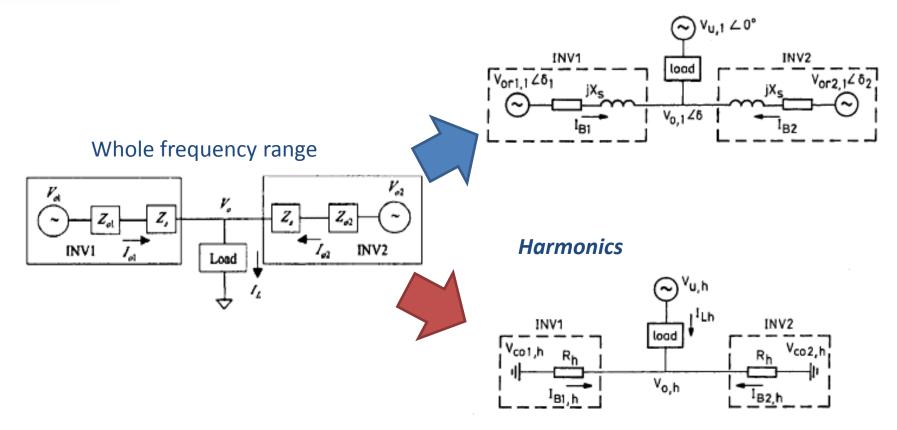
$$Z_{V}(s) = L_{D} \frac{2k_{1}s^{2}}{s^{2} + 2k_{1}s + \omega_{1}^{2}} + \sum_{\substack{i=3\\odd}}^{n} R_{i} \frac{2k_{i}s}{s^{2} + 2k_{i}s + \omega_{i}^{2}}$$





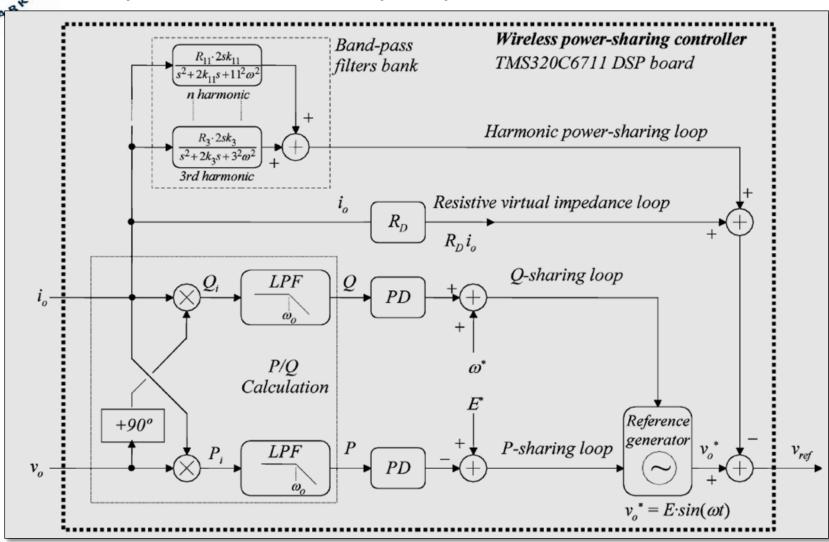
Harmonic current sharing

Fundamental



Parallel Control of the UPS Inverters With Frequency-dependent Droop Scheme S. J. Chiang and J. M. Chang

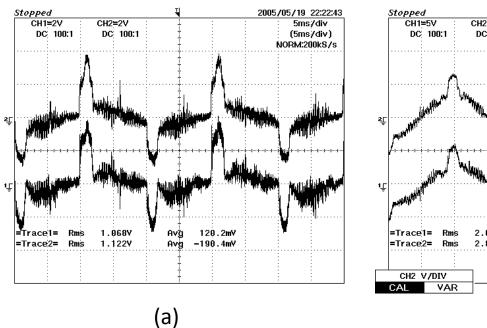
Droop method with virtual output impedance and selective harmonic

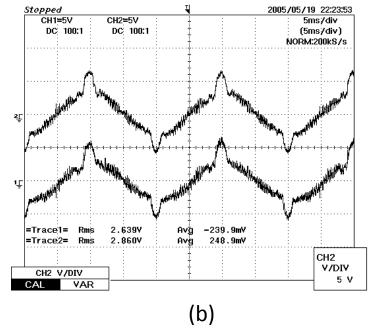


DSP implementation is appropriate for the multi-loop droop framework



Droop method for resistive output impedance

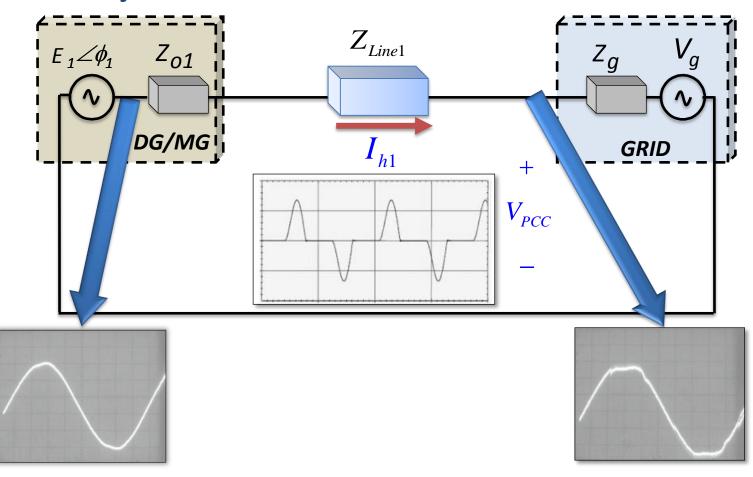




- (a) Nonlinear load, Y: 2 A/div, X: 5 ms/div;
- (b) Resistive // nonlineal load, Y: 10 A/div, X: 5 ms/div.



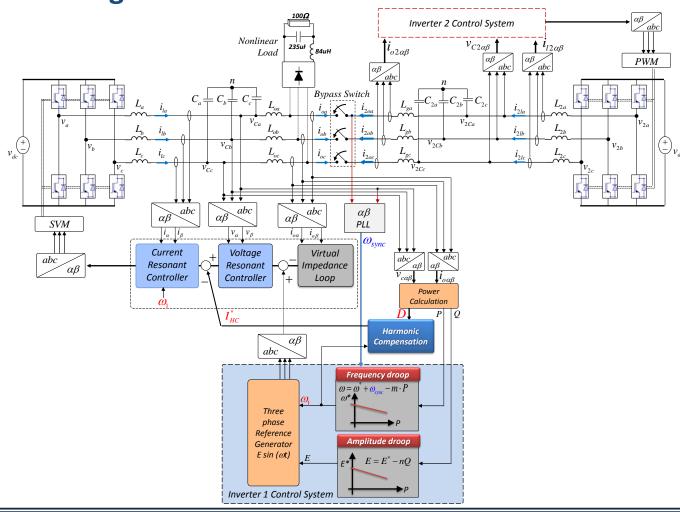
Voltage harmonic reduction by using current harmonics injection



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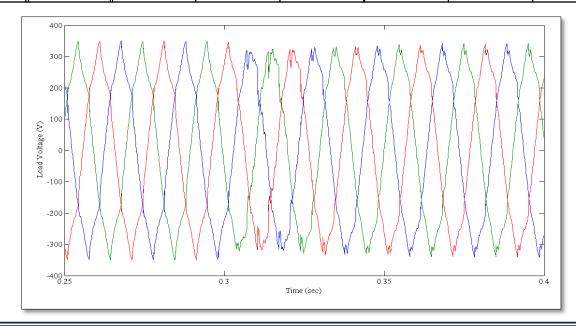
Decentralized voltage harmonic reduction in an islanded microgrid





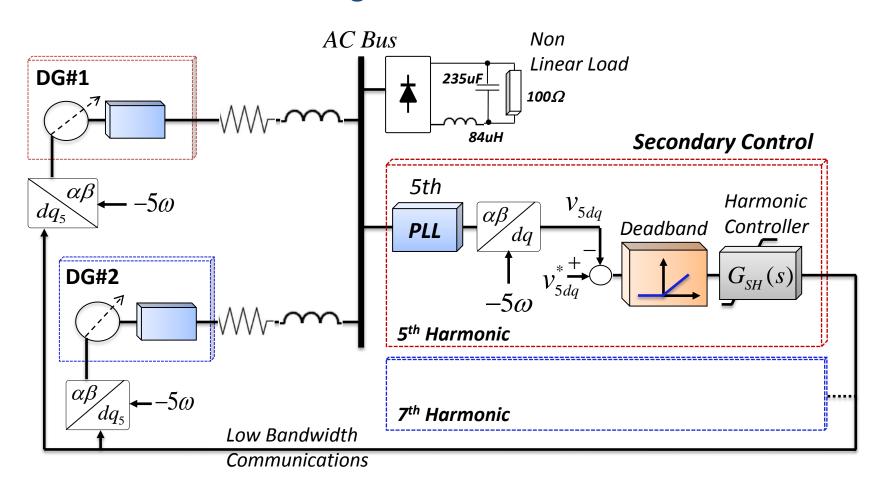
Decentralized voltage harmonic reduction in an islanded microgrid

		Before Compensation		After Compensation			
		THD%	5 th %	7 th %	THD%	5 th %	7 th %
Voltage	DG1	3.8	2.9	2.0	1.2	0.6	0.5
	DG2	2.9	2.1	1.5	1.1	0.5	0.4
	Load	5.3	4.2	2.8	3.2	2.3	2.0
Current	DG1	58.6	53.7	22.9	87.6	75.2	44.1
	DG2	45.8	41.5	18.8	44.5	38.1	22.5
	Load	52.2	45.6	20.9	66.1	56.7	33.3



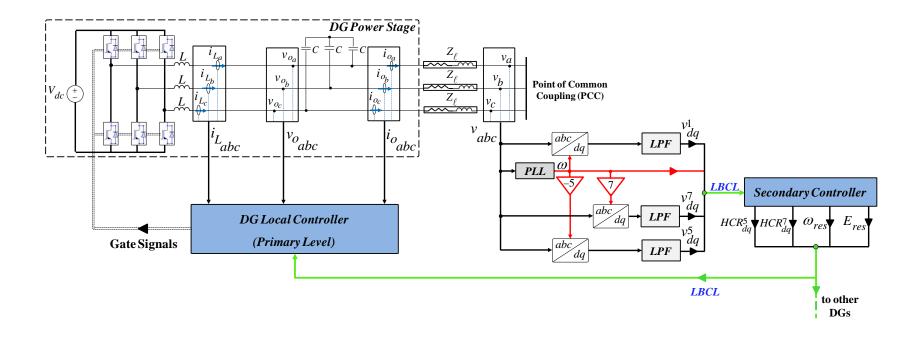


Secondary control for voltage harmonic distribution in islanded microgrids



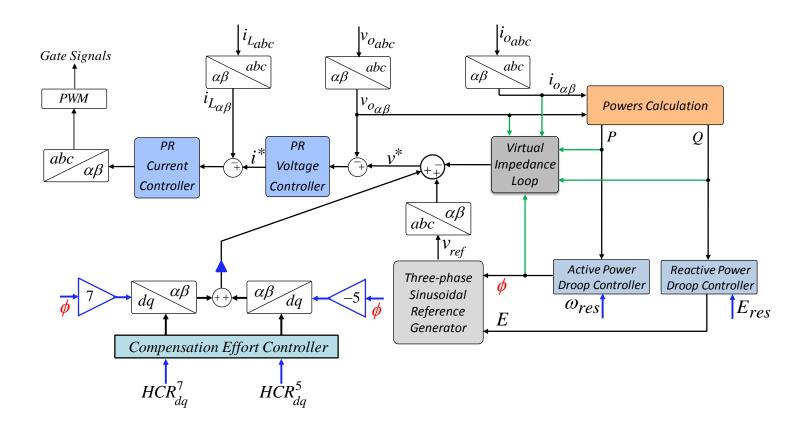
Harmonics in Microgrids

Secondary control for voltage harmonic compensation in islanded Microgrids



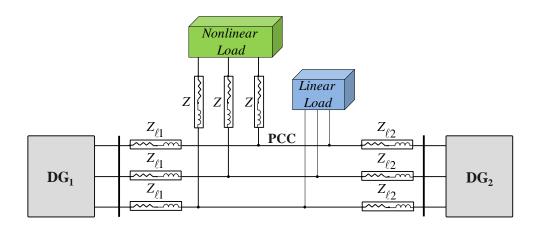
Harmonics in Microgrids

DG local controller (primary level)





Test system for secondary harmonic compensation

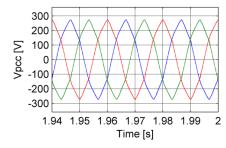




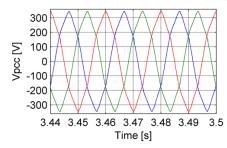
Harmonics in Microgrids

Secondary control for voltage harmonic compensation

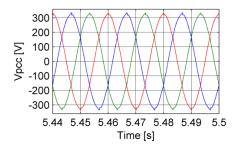
(a) Before amplitude restoration and harmonic compensation



(b) After amplitude restoration (no harmonic compensation)



(c) After amplitude restoration and harmonic compensation





Voltage unbalance definition

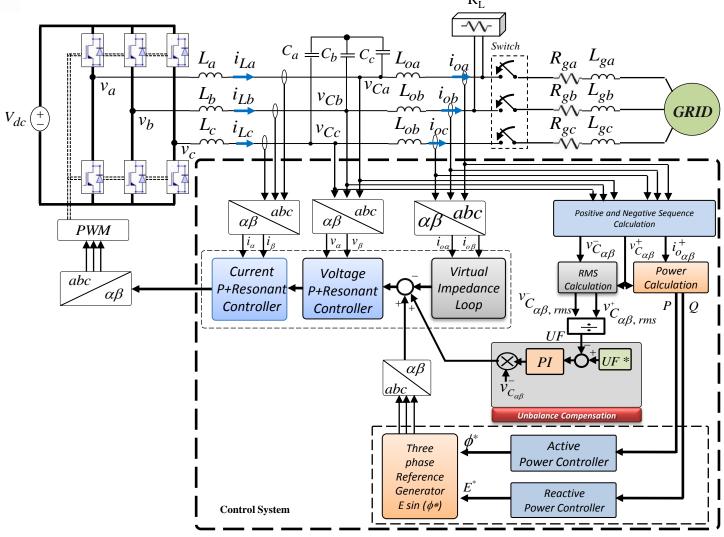
Voltage unbalance factor (*UF*) is considered as the index of unbalance. *UF* can be defined as follows:

$$UF = \frac{v_{C_{\alpha\beta,rms}}^{-}}{v_{C_{\alpha\beta,rms}}^{+}}$$

where ${}^{v}\bar{C}_{\alpha\beta,rms}$ and ${}^{v}\bar{C}_{\alpha\beta,rms}$ are rms values of negative and positive sequences of the DG output voltage.



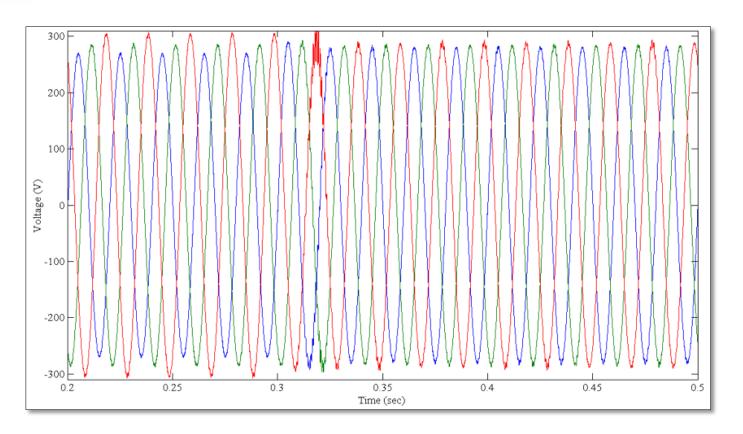
Unbalance compensation for a grid-connected DG



18-Aug-11



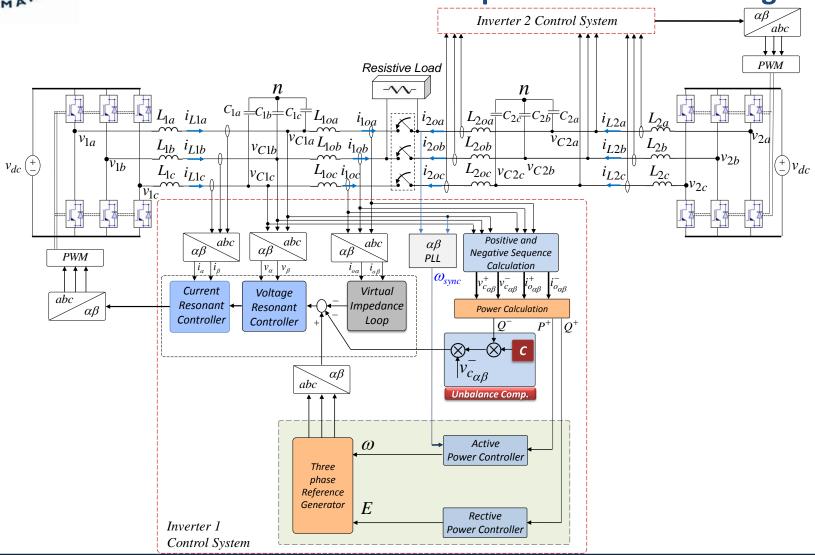
Unbalance compensation for a grid-connected DG



DG output Voltage



Decentralized unbalance compensation for a microgrid

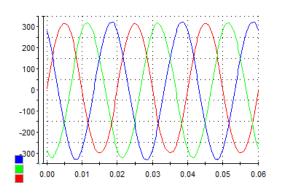


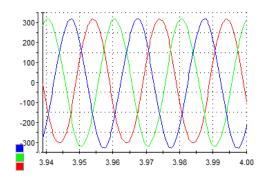
18-Aug-11

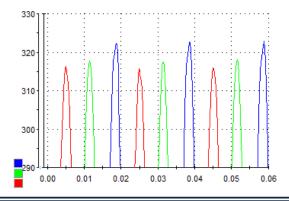


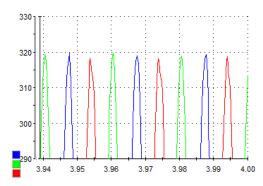
Decentralized unbalance compensation for a microgrid Unsymmetrical line

Voltage after and before compensation





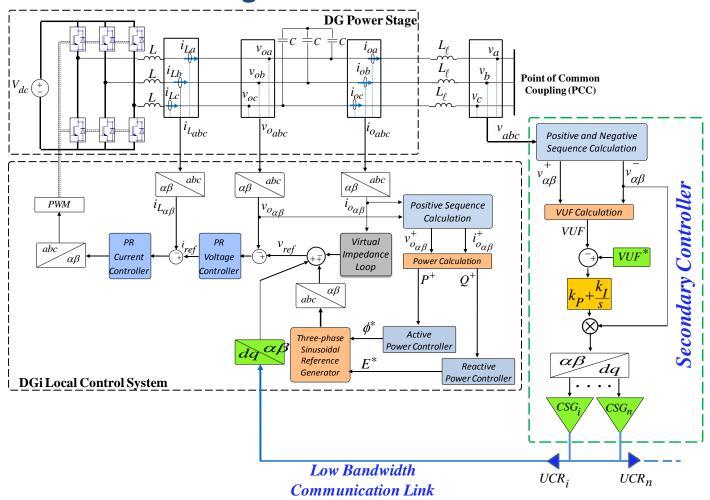




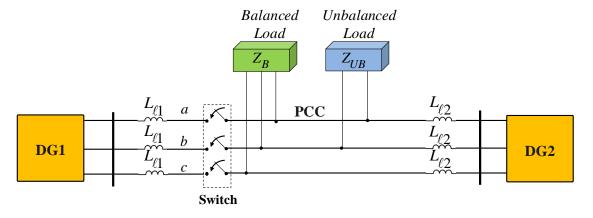
18-Aug-11

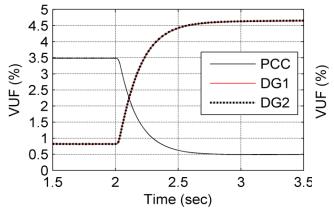


Secondary control for unbalance compensation in islanded Microgrids

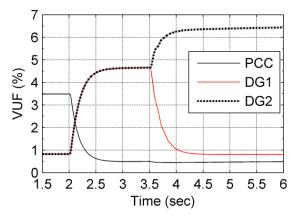








PCC
DG1
1.5
2
2.5
1.5
0
1.5
2
2.5
3
3.5
Time (sec)

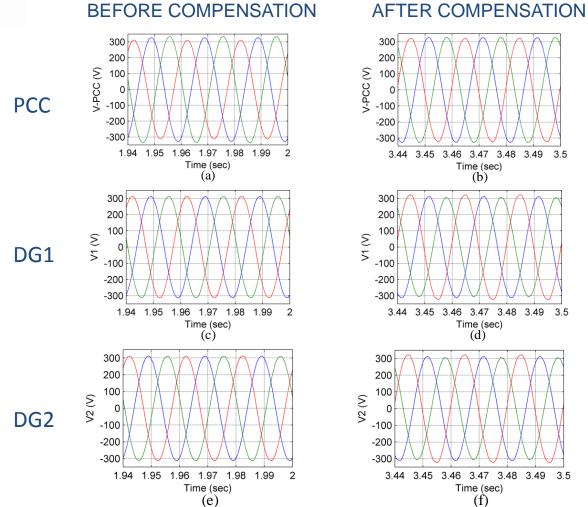


VUF at PCC and DGs terminal $(CSG_1=CSG_2=1)$

VUF at PCC and DGs terminal $(CSG_1=1,CSG_2=1.25)$

VUF at PCC and DGs terminal (DG1 communication link failure at t=3.5sec)





Three-phase voltage waveforms

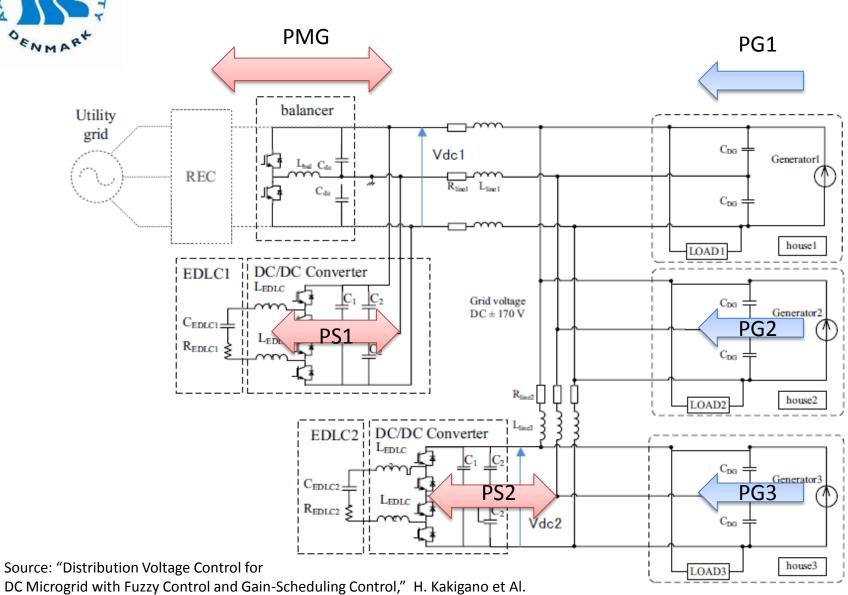


Conclusions

- Voltage harmonics in microgrids can be reduced by injecting current harmonic or adjusting harmonic voltage in the DG terminals
- Secondary control can be used to close the loop of the harmonic voltage compensation in the microgrid
- Tertiary control can be used to reduce the current harmonics injected by the microgrid to the grid
- A ponderated trade off between the secondary and tertiary controls have to be designed
- Unbalances in microgrids can be reduced by injecting a voltage negative sequence in the DG proportional to Q negative sequence
- Secondary control and tertiary control for unbalance compensation can be used for islanding and grid-connected microgrids.
- Reactive power have to be limited and ponderated for harmonics and unbalance compensation.



Distributed Energy Storage in Microgrids



18-Aug-11



Distributed Energy Storage in Microgrids

Adaptive droop control:

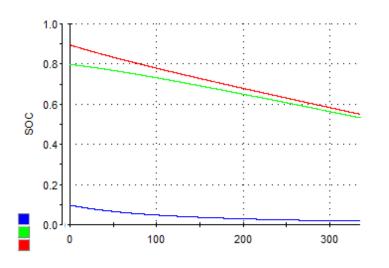
DC: $V=V^* - (k/SoC)Io$

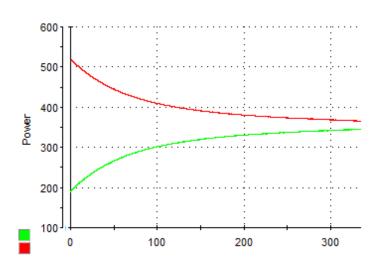
AC: $\omega = \omega^* - (k/SoC)P$

Extended Kalman Fiters are used to obtain the SoC of the batteries.

SOC1 - SOC2

PS1, PS2





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Experience	• Sept 2004 ~Aug 2008 Lecturer professor (Technical University of Catalonia, Barcelona)				
	• Sept 2008~Present Associate Professor (now part time) (Technical University of Catalonia,				
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Autobiography

Josep M. Guerrero (S'01–M'04–SM'08) was born in Barcelona, Spain, in 1973. He received the B.S. degree in telecommunications engineering, the M.S. degree in electronics engineering, and the Ph.D. degree in power electronics from the Technical University of Catalonia, Barcelona, Spain, in 1997, 2000 and 2003, respectively.

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